

## Active HDTV Reconstruction Filter Lowers Cost and Improves Performance

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Historically these filters have been passive L-C types. Because of the steep selectivity of the filter, they require group-delay compensation. They also require gain to drive the back-terminated output and isolate the filter and DAC from the load. Because of the difficulty, custom parts, and tuning required, L-C filters used for these applications are usually purchased at significant cost, which is further burdened by the cost of buffering.

The active-reconstruction filter shown is a lower cost alternative. It uses R-C components, and provides a 30MHz bandwidth with >40dB rejection at 74.25MHz, as well as group-delay compensation and buffering using a single [MAX4383](#) quad op amp. The improved tolerance of capacitors (1% to 2%) vs. inductors (5% to 10%) removes the need for production tuning, although initial component selection is required to account for the parasitic components of the PCB. The circuit shown uses dual supplies, but the [MAX4383](#) can use a single supply with appropriate biasing.

The filter itself is a Rauch, or multiple-feedback realization of a 5-pole, modified Butterworth characteristic using one first-order +6dB gain stage and two 2nd-order unity-gain stages. The final stage is a first-order group-delay compensator, which drives the back-terminated 75 $\Omega$  load to an overall unity gain. There are three sensitive points in the circuit: R14, which controls the real pole, C1 and C4, which control the high-frequency poles, and C5, which sets the GD compensation.

R16 and a connector are provided at the input, in place of the DAC load, for measurement purposes. The DAC load resistance is typically 35 $\Omega$  to 40 $\Omega$ , on which the values of R14 and R16 are based. In some MPEG decoders, this may be as high as 150 $\Omega$  to 160 $\Omega$ . To accommodate different DAC load resistors, adjust the value of the R14, C6 product accordingly.

The values of C1 and C4 are small with respect to the PCB's parasitic capacity, and reflect a two-sided PCB on FR-4 with 1/2oz, 3/16in-thick copper with a ground plane around the op-amp pins. C5 adjusts the GD compensation. In laying out this circuit, it is best to adjust the values of C1 and C4 for bandwidth before adjusting C5 for GD-variation compensation. A fully configured PCB is available<sup>[1]</sup>.

## Schematic of Filter

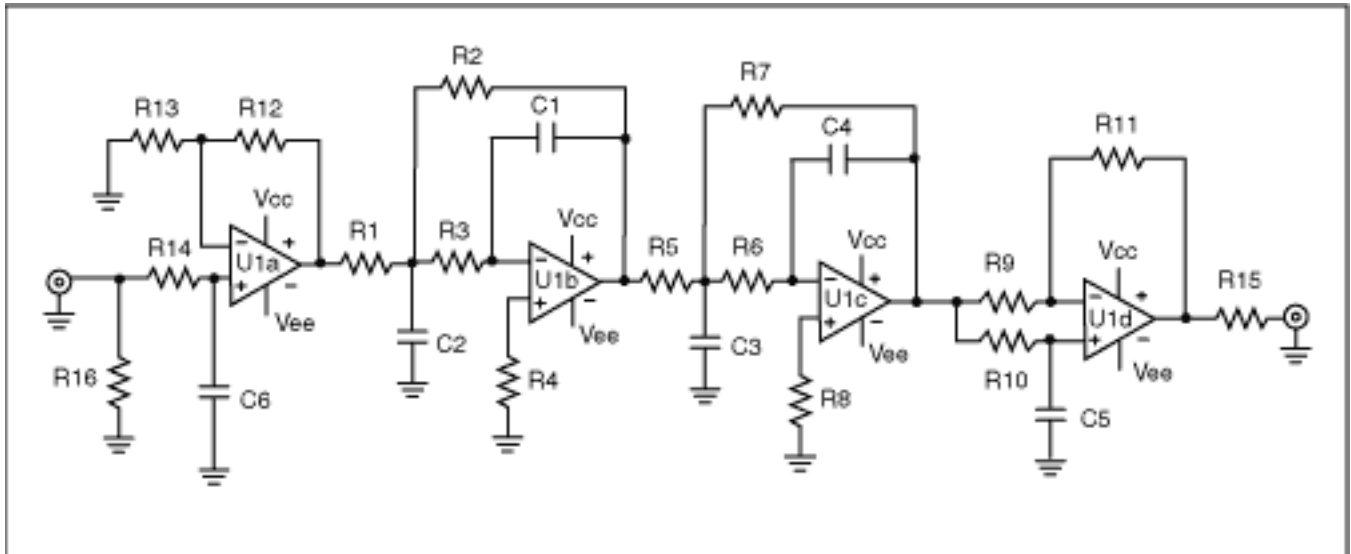


Figure 1. A 5-Pole Active Filter for HDTV and Progressive DVD-Reconstruction Applications.

## Component List

Designation	Qty.	Description
C1, C4	1	3.3pF, 50V C0G, $\pm 0.25$ pF ceramic capacitor (0603)TDK C1608COG1H3R3CT
C2	1	68pF, 50V G0G, 5% ceramic capacitor (0603)Murata GRM11885C1H680J or TDK C1608C0G1H680JT
C3	1	27pF, 50 C0G, 5% ceramic capacitor (0603)Murata GRM1885C1H270J or TDK C1608C0G1H270JT
C5	1	33pF, 50V C0G, 5% ceramic capacitor (0603)Taiyo Yuden UMK107CG330JZ or TDK C1608C0G1H330JT
C6	1	100pF, 50V C0G, 5% ceramic capacitor (0603)Murata GRM1885C1H101JT TDK C1608C0G1H101JT
C7, C9	2	10 $\mu$ F, 6.3V X5R, 20% ceramic capacitor (0805)Taiyo Yuden JMK212BJ106MG or TDK C2012X5R0J106M
C8, C10	2	0.1 $\mu$ F, 25V X7R, 10% ceramic capacitor (0603)TDK C1608X7R1E104K

R1, R2, R4, R5, R7-R13	11	210 $\Omega$ , 1% resistor (0805)
R3	1	100 $\Omega$ , 1% resistor (0805)
R6	1	121 $\Omega$ , 1% resistor (0805)
R14, R15, R16	3	75 $\Omega$ , 1% resistor (0805)
R17	0	Not installed, resistor (0805)
U1	1	MAX4383EUP (14-pin TSSOP)
INPUT, OUTPUT	2	BNC connectors
None	1	MAX4383 PC board

## Typical Applications Circuit

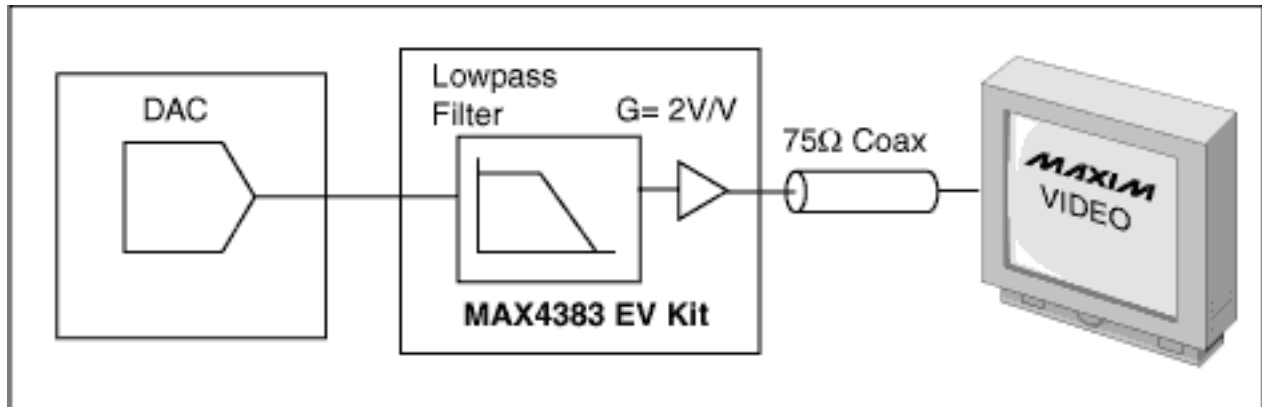


Figure 2.

## Gain vs. Frequency

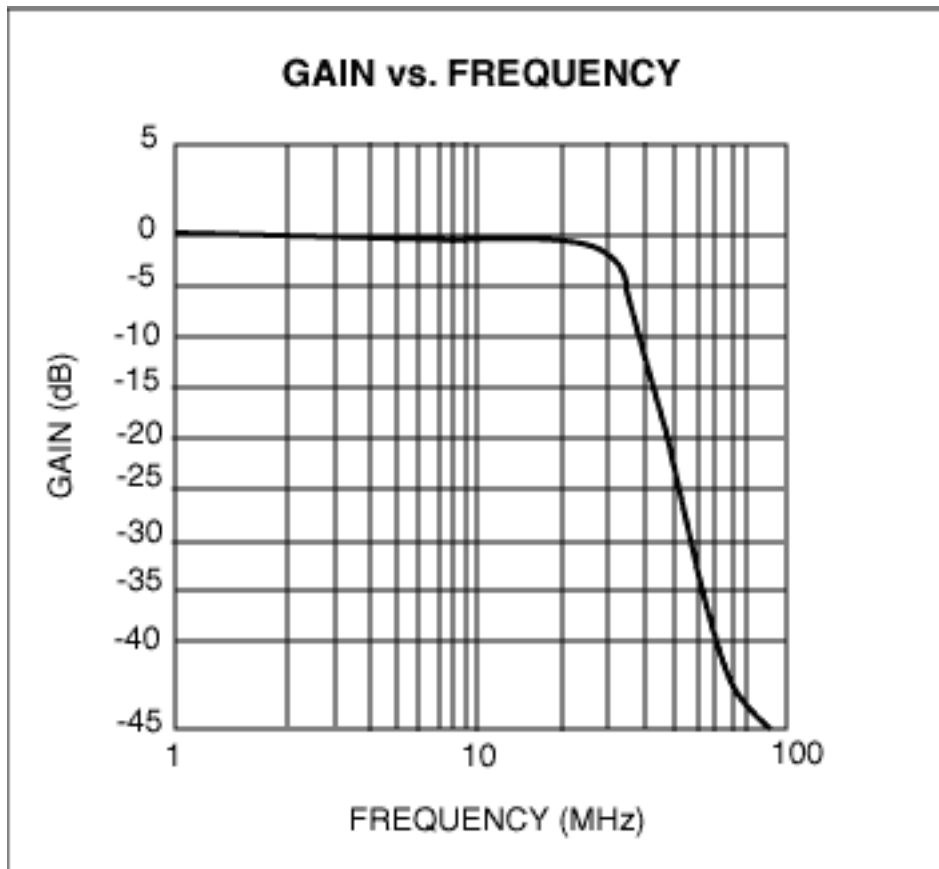


Figure 3.

[1] MAX4383 EV kit

### More Information

MAX4383: [QuickView](#) -- [Full \(PDF\) Data Sheet](#) -- [Free Samples](#)

MAX4383EVkit: [QuickView](#) -- [Full \(PDF\) Data Sheet](#)